

Curriculum Vitae



Professor Attallah Samir

Dean

School of Science and Technology

Tel : +65 6248 9134

Education Qualifications

1996	PhD (Electrical Engineering) University of Bordeaux I, France
1993	MSc (Electrical Engineering) University of Bordeaux I, France

Academic and Professional Experience

2016 - Present	Dean, School of Science and Technology (SST)
2010 - 2015	Vice-Dean in charge of Academic Quality Assurance and Accreditation, SST
2010 - 2015	Chair, Internal Audit Committee (IAC), UNISIM
2012	Chair, International Conference on Intelligent Unmanned Systems (8th ICIUS), UniSIM
2010	Member of the Engineering Accreditation Board (EAB) task force, IES
2008 - 2011	Head, Electronics Engineering Programme (SIM)
2008 - 2018	Associate Professor, SST
2006	Visiting Professor, Ecole Nationale Supérieure des Télécommunications (ENST), Télécom Paris, France
2003 - 2007	Assistant Professor, Electrical & Computer Engineering Department (ECE), National University of Singapore (NUS)
2007	Supervisor, Digital Communications Lab, ECE Department, NUS
2001 - 2003	Senior Lecturer, Department of Electronics & Computer Engineering, Curtin University of Technology, Perth, Western Australia
1998 - 2001	Member/Senior Member Technical Staff (Research), Centre for Wireless Communications, NUS

Memberships and Professional Activities

- IEEE Senior Member since 2001
- Member of several international conference committees
- Vice-chairman IEEE communications chapter, Western Australia

Consultation and Executive Experience

- Served as a consultant and research assessor for several local and international organizations.

Research Interests

- General area of digital signal processing and communications.

Selected Publications

- S. Attallah, "Wavelet Transform-Domain LMS Adaptive Filter with Partial Subband-Coefficient Updating," IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing, vol. 53, Issue 1, Jan. 2006.
- S. Attallah, "The Generalized Rayleigh's Quotient Adaptive Noise Subspace Algorithm: A Householder Transformation based Implementation," IEEE Trans. on Circuits and Systems II: Analog and Digital Signal Processing, vol. 53, Issue 1, Jan. 2006.
- S. Attallah, J. H. Manton and K. Abed-Meraim, "Convergence Analysis of the NOJA Algorithm using the ODE Approach," Journal of Signal Processing, Elsevier, vol. 86, Issue 11, Nov. 2006.
- Li Mi, S. Attallah and A. Nallanathan "A Low-cost Blind Carrier Frequency Offset Estimator for Down-Link MIMO Multi-carrier System," IEE Proceedings on Communications, vol. 153, no. 6, Dec. 2006.
- B. T. Lokesh, Y. C. Liang and S. Attallah, "Reconfigurable Transceivers for Wireless Broadband Access Schemes," IEEE Wireless Communications Magazine, vol. 14, no. 3, June 2007.
- S. Attallah, Wu Yan and J.W.M Bergmans, "Low Complexity Blind Estimation of Residual Carrier Offset in OFDM-based Wireless LAN Systems," IEE Proceedings on Communications, vol. 1, issue 4, pp. 604-611, Aug. 2007.
- S. Attallah, B. Lokesh, F. Hongyi and Y. C. Liang, "Joint Channel and Carrier Offset Estimation for Synchronous Uplink CDMA Systems," IEEE Trans. Vehicular Technology, vol. 56, pp. 1-6, Sept. 2007.
- L. Yang, S. Attallah, G. Mathew and K. Abed-Meraim "Analysis of Orthogonality Error Propagation for FRANS and HFRANS Algorithms," IEEE Trans. on Signal Processing, vol. 56, pp. 4515-4521, Sept. 2008.
- B. T. Lokesh, S. Attallah, K. Abed-Meraim, Y. C. Liang and F. Hongyi, "Non-Data Aided Joint Carrier frequency Offset and Channel Estimation for Uplink MC-CDMA System," IEEE Trans. on Signal Processing, vol. 56, pp. 4398-4408, Sept. 2008.
- S. Attallah and K. Abed-Meraim, "A Fast Adaptive Algorithm for the Generalized Symmetric Eigen-value Problem," IEEE Signal Processing Letters, vol. 15, pp. 797-800, 2008.
- Wu Yan, S. Attallah and J.W.M Bergmans, "On the Optimality of Null-Subcarrier Placement for Blind Carrier Frequency Offset Estimation in OFDM Systems," IEEE Trans. Vehicular Technology, vol. 58, pp. 2109-2115, May 2009.
- L. Yang, S. Attallah "Adaptive Noise Subspace Estimation Algorithm Suitable for VLSI Implementation," IEEE Signal Processing Letters, vol. 16, pp. 1075-1078, Dec. 2009.

Updated on 8 November 2018